

Amendments to the Specification

Please replace these paragraphs with the following amended paragraphs showing deletions (~~strike through~~) and insertions (underline).

Page 4, lines 6-9:

Figure 1 illustrates an overview of a system incorporated with the teachings of one embodiment of the present invention, including a controller equipped to suppress or ~~ignore-negate~~ power button and device wake events during AC absence;

Please replace these paragraphs with the following amended paragraphs showing deletions (~~strike through~~) and insertions (underline).

Page 7, lines 19-25:

Additionally, MCH/ICH/BB **108** includes in particular terminals (e.g. pins (not shown)) for receiving one or more signals **144** denoting one or more power button related and/or device wake events. Further, MCH/ICH/BB **108** includes logic (not shown) for suppressing or ~~ignoring-negating~~ these signals, when system **100** is powered by DC power source **132** in a suspended to memory state, during absence of AC. More specifically, for the embodiment, the logic is implemented in the ICH portion of MCH/ICH/BB **108**.

Page 8, lines 5-11:

By suppressing or ~~ignoring~~ ~~negating~~ power button and wake device events **144** when system **100** is powered by DC power source **132** in a suspended to memory state, during absence of AC, system **100** is prevented from starting up or waking up and becoming a large load for DC power source **132**. Resultantly, the capacity of integral back up DC power source **132** may be smaller, and less costly. In turn, system **100** may be provided with integral backup power, more specifically, integral DC backup power **132**, in a more cost effective manner.

Page 11, lines 4-10:

From “suspended to memory with a persistent copy of system state saved” state **218**, system **100** may transition back to either “visual on” state **212** or “visual off” state **214** in response to AC power re-presence, or a power button/device wake event **232** and **234** when AC is present (state **218** entered due to inactivity). As described earlier, power button or device wake events are advantageously suppressed or ~~ignored~~ ~~negated~~, while system **100** is in suspended to memory state **128** during AC absence.

Page 15, lines 1-11:

Thus, power button/device wake event signal **144** will be negated, during AC absence, since AC presence/absence signal **136** assumes logic “0”. Accordingly, the push button/device wake event may be suppressed or ~~ignored~~ ~~negated~~ when AC is absent at power supply **116**.

For the embodiment, MCH/ICH/BB **108** is provided with and employs presence/absence signal **136** directly in the suppression/~~ignoring~~ ~~negation~~ of a power button/device wake event signal **144**. However, alternate embodiments may be

practiced with power button/device wake event signal **144** being suppressed or ~~ignored~~
negated, employing a AC presence/absence state signal, that is generated by another
element other than power supply **116** based on AC presence/absence signal **136**
generated by power supply **116**.